

## UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/623,983	07/21/2003	Zhijian Xie	3-4	5529
7	590 03/25/200		EXAMINER	
Ryan, Mason & Lewis, LLP 90 Forest Avenue			ABRAHAM, FETSUM	
Locust Valley, NY 11560			ART UNIT	PAPER NUMBER
,			2826	•

DATE MAILED: 03/25/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

			1			
	Application No.	Applicant(s)				
	10/623,983	XIE ET AL.				
Office Action Summary	Examiner	Art Unit				
	Fetsum Abraham	2826				
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REI THE MAILING DATE OF THIS COMMUNICATION  Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  If the period for reply specified above is less than thirty (30) days, a  If NO period for reply is specified above, the maximum statutory perions  Failure to reply within the set or extended period for reply will, by state any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a least reply within the statutory minimum of thir ided will apply and will expire SIX (6) MON tute, cause the application to become Al	reply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communi BANDONED (35 U.S.C. § 133).	cation.			
Status						
1) Responsive to communication(s) filed on _						
2a) This action is <b>FINAL</b> . 2b) ⊠ T	his action is non-final.					
3) Since this application is in condition for allow	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice unde	er Ex parte Quayle, 1935 C.D	). 11, 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-18 is/are pending in the application	on.					
4a) Of the above claim(s) is/are without	frawn from consideration.					
5) Claim(s) is/are allowed.						
6) Claim(s) 1-18 is/are rejected.						
7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and	d/or election requirement					
· · · · · · · · · · · · · · · · · · ·	aror election requirement.					
Application Papers						
9) The specification is objected to by the Exam		h. die E. e. e. e.				
10) The drawing(s) filed on is/are: a) a						
Applicant may not request that any objection to t Replacement drawing sheet(s) including the con			21(d)			
11) The oath or declaration is objected to by the	•					
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of:	ign priority under 35 U.S.C. {	§ 119(a)-(d) or (f).				
1. Certified copies of the priority docume	ents have been received.					
2. Certified copies of the priority docume	ents have been received in A	application No				
<ol><li>Copies of the certified copies of the p</li></ol>	riority documents have been	received in this National Stage	€			
application from the International Bur						
* See the attached detailed Office action for a	list of the certified copies not	received.				
<b>1.</b> 10						
11 - K = 0	······					
Attachment(s)  1) Notice of References Cited (PTO-892)	4) Interview	Summary (PTO-413)				
2) Notice of References Ched (PTO-092)  Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(	s)/Mail Date				
<ol> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/ Paper No(s)/Mail Date</li> </ol>	08) 5)  Notice of I 6)  Other:	nformal Patent Application (PTO-152) 				

## Claims rejection

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the claimed shielding layer and its physical relationship with other device elements must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Furthermore, there is no vertical DMOD in the drawings (as in claim 9) for proper examination and the shielding layer (as in claim 10) must be shown in the drawings first for proper examination to continue on the claimed device.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1-18 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Said first source/drain and second source/drain regions in the claims is not specifically addressed to imply a single or two transistor/s. If the structure is a single device, the expressions should change to read first source and first drain or source and drain. Based on the drawings, it so seems like the intent is to address a single power MOSFET.

Application/Control Number: 10/623,983

Art Unit: 2826

Furthermore, there is no vertical DMOD in the drawings (as in claim 9) for proper examination and the shielding layer (as in claim 10) must be shown in the drawings first for proper examination to continue on the claimed device.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-18, so far as understood, is rejected under 35 U.S.C. 103(a) as being unpatentable over Morikawa et al (6,707,102).

As for claims 1,6-8,14,16 the patent discloses a diffused power LDMOS in the front page composed of source (5), drain (9), a gate (3) formed on the surface of the substrate between the source and drain regions, and a shielding layer that does not overlap with the gate on the substrate between the gate and the drain regions and specifically over the offset region of the drain (see abstract). Although the claim language is not clear as indicated above, it would have been obvious to one skilled in the art to conclude the claimed invention as a duplicate of the prior art based on structural correlation between the two structural.

As for claims 2-4,15, the shielding layer is connected to the offset region of the drain through a conductive trace via a hole and insulated from the substrate (see abstract). As for claim 4, multi level metalization is common in the art specifically when thick interlayer insulation is deposed on the substrate. Therefore, it would have been

Application/Control Number: 10/623,983

Art Unit: 2826

obvious to use multiple metal layers for contact, since the method avoids the difficulty of accessing a node in small devices.

As for claim 5, the most common insulator in the art oxides.

As for claims 11,13, the interconnection of the shielding layer through a via signifies the claimed plug in claim 11.

As for claims 12,18, there is a second gate layer on the gate electrode in the prior art and that configuration reduces gate resistance.

As for claim 17, the overall structure suggests a capacitor formed between the gate and the overall shielding layer attached to the drain but the capacitance is low because of the wide gap between the two elements.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fetsum Abraham whose telephone number is: 571-272-1911. The examiner can normally be reached on 8:00 - 18:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached at 571-272-1915.

Fetsum Abraham

3/19/04